

EIA	Description	DTE DIR	DTE DB25	DTE DB9	WALL CABLE	EXT CABLE	13260A P2	13232A	13232B	13232C	13232N	13232X ICA	12828A	12880-60003
AA	Protective Ground	↔	1				A						1	1
BA	Transmitted Data	→	2	3	Green	Green	B	2	X	2	2	2	2	2
BB	Received Data	←	3	2	Red	Red	C	3	W	3	3	3	3	3
CA	Request to Send	→	4	7	Black	Black	D	4		4	4	4	4	
CB	Clear to Send	←	5	8	Yellow	Yellow	E	5	U	5	5	5	5	
CC	Data Set Ready	←	6	6	White	White	F	6			6	6	6	6
AB	Signal Ground	↔	7	5	Shield	Shield	H	7	24	7	7	7	7	7
CF	Data Carrier Detect	←	8	1			J	8		8	8	8	8	
–	+12 Volts	↔	9				–						9	
–	–12 Volts	↔	10				–						10	
–	Select Transmit Channel (V.24)	→	11				–							
SCF	Secondary Data Carrier Detect	←	12				N	12		12	12	12		
SCB	Secondary Clear to Send	←	13				–							
SBA	Secondary Transmitted Data	→	14				–							
DB	Transmitter Clock (DCE)	←	15				[12]				15			
SBB	Secondary Received Data	←	16				–							
DD	Receiver Clock	←	17				[13]				17			
LL	Local Loopback	→	18				–							
SCA	Secondary Request to Send	→	19				M	11		11	11	11		
CD	Data Terminal Ready	→	20	4	Blue		P	20		20	20	20	20	
CG	Signal Quality Detect	←	21				–							
CE	Ring Indicator	←	22	9			[14]				22	22		
CH	Data Rate Selector	→	23				[R]				23	23		
CI		←												
DA	Transmitter Clock (DTE)	→	24				[S]				24			
TM	Test Mode	←	25				–							
–	×8 Clock Out	→	–			Blue	K		L	16		16		16
–	×16 Clock Out	→	–				L			24		24		
–	×16 Clock In	←	–				S			25		25		

Logic 1 is –3 to –25 volts

Logic 0 is +3 to +25 volts

Open circuit = logic 1